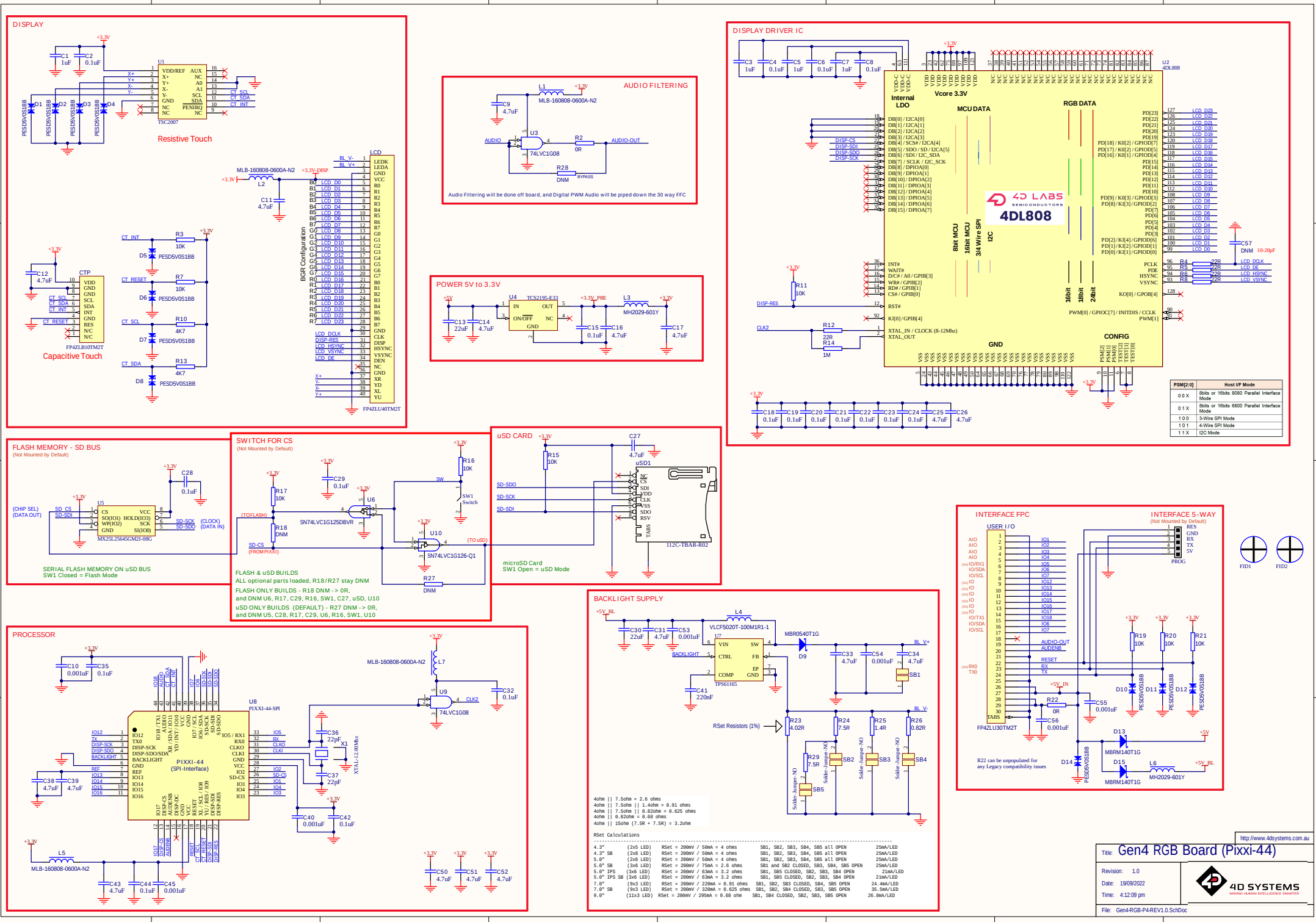
DISPLAY
Resistive Touch
Capacitive Touch
AUDIO FILTERING
Audio Filtering will be done off board, and Digital PWM Audio will be piped down the 30 way FFC
POWER 5V to 3.3V
DISPLAY DRIVER IC
4DL808
FLASH MEMORY - SD BUS
(Not Mounted by Default)
SERIAL FLASH MEMORY ON uSD BUS
SW1 Closed = Flash Mode
SWITCH FOR CS
(Not Mounted by Default)
FLASH & uSD BUILDS
ALL optional parts loaded, R18/R27 stay DNM
FLASH ONLY BUILDS - R18 DNM -> 0R,
and DNM U6, R17, C29, R16, SW1, C27, uSD, U10
uSD ONLY BUILDS (DEFAULT) - R27 DNM -> 0R,
and DNM U5, C28, R17, C29, U6, R16, SW1, U10
uSD CARD
microSD Card
SW1 Open = uSD Mode
PROCESSOR
PIXXI-44
(SPI-Interface)
BACKLIGHT SUPPLY
RSet Resistors (1%)
INTERFACE FPC
USER I/O
INTERFACE 5-WAY
(Not Mounted by Default)
R22 can be unpopulated for
any Legacy compatibility issues
http://www.4dsystems.com.au
Title: Gen4 RGB Board (Pixxi-44)
Revision: 1.0
Date: 19/09/2022
Time: 4:12:09 pm
File: Gen4-RGB-P4-REV1.0.SchDoc
4D SYSTEMS
MAKING HUMAN INTELLIGENCE SMARTER