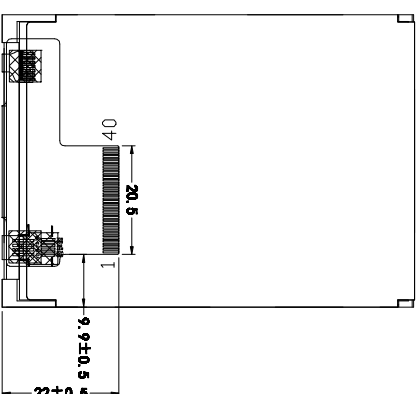
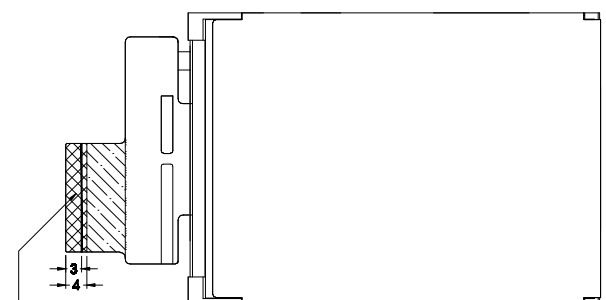
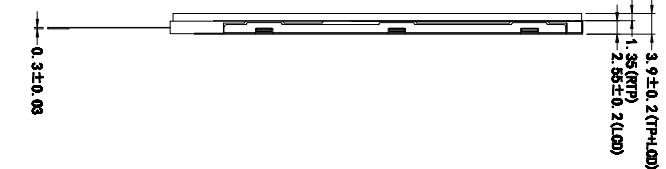
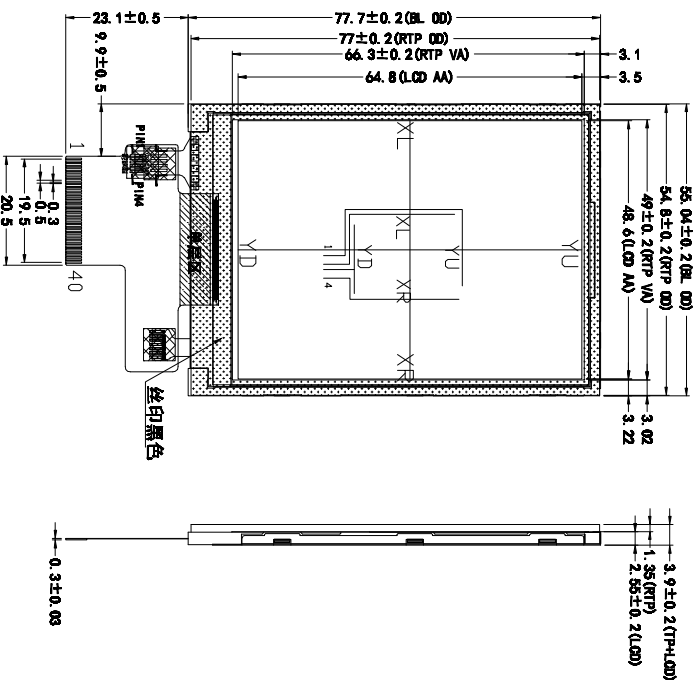


Revisits	Revision Note	Date	Signature (Checked)
1			
2			
3			

Pin	Description
1	GND
2	GND/(SP1)
3	IM0
4	FMARK
5	YD
6	XL
7	RESET
8	RS/(SCL)
9	CS
10	RD
11	WR/(D/C)
12	VCC
13	NC/(SDD)
14	GND
15	DB15/(GND)
16	DB14/(GND)
17	DB13/(GND)
18	DB12/(GND)
19	DB11/(GND)
20	DB10/(GND)
21	DB9/(GND)
22	DB8/(GND)
23	DB7/(GND)
24	DB6/(GND)
25	DB0/(GND)
26	DB1/(GND)
27	DB2/(GND)
28	DB3/(GND)
29	DB4/(GND)
30	DB5/(GND)
31	VU
32	XR
33	LEDA
34	LEDK1
35	LEDK2
36	LEDK3
37	LEDK4
38	LEDK5
39	LEDK6
40	GND

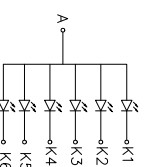


NO.	SYMBOL
1	YU
2	XL
3	YD
4	XR

0.3+0.03

Notes:
1 Turn

1. Tryer/Film to Glass
2. Operating Voltage: $\sim 10V$
3. Operating Temperature: $-10^{\circ}C$ - $-60^{\circ}C$
4. Storage Temperature: $-50^{\circ}C$ - $-70^{\circ}C$
5. Life Time: $\sim 1,000,000$ Lines
6. Carrier Material: TOPC
7. Exposure Times: $\sim 10ms$
7. Linearity: $\sim 1.5\%$
8. Transmittance: $\sim 80\%$
9. Surface Hardness: $\sim 5H$ (Pencil)
10. Operation Power: $50W$ - $120W$
11. Resolunar:
 - ITO/Film μ : 1800 - 5000
 - Stand and TO/Glass: 2800 - 8500
12. Unpublished: ~ 1000000 Lines



BACKLIGHT CIRCUIT DIAGRAM

40	GND
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Tableau Code	Signature	Date
Limits Unless Otherwise Noted		

Tolerance		Grade	图例	备注
Grade	○		竣工	
0 - 150	±0.10	±0.15	±0.20	审核
150+	±0.20	±0.25	±0.30	批准
Angle	±0.50°	±0.80°		客户确认

材料		4D SYSTEMS PTY LTD	
单位	mm		
比例	1:1		
日期	20190909	客户图号	
		工程图号	4DLCD-3224Q320-RTP
投影法			版本 1.0

由 Autodesk 教育版产品制作

由 Autodesk 教育版产品制作

一、产品特征 (Features):

1. LCD类型 (Resolution): 240×320
2. 分辨率 (Resolution): 240×320
3. 显示色彩 (Color): 262K
4. 驱动IC (Driver IC): ILI9341V
5. 入眼观察视角 (Viewing angle): 6° @CLK
6. 接口类型 (Interface Type): MCU6-b/s/ST
7. 背光类型 (Backlight Type): 6颗LED灯并连, 电压: 3.3V 电流: 120mA
8. 操作温度 (Operating temperature): -20℃ ~ 70℃
9. 储存温度 (Storage Temperature): -30℃ ~ 80℃
10. 储存湿度 (Storage Humidity): 100% ~ 40RH
11. 产品所有物料符合RoHS标准 (All materials comply with RoHS standard).
12. 验证: 手机(硬件)可拆面胶, 不要粘到触摸屏铜线, 以免影响IT性能及使用寿命
13. Vmax: 手机(硬件)可拆面胶, 建议外壳可拆区域比Vmax单边小0.5mm以上
14. 未注尺寸公差: ±0.2mm
- 二、防静电PCB A设计建议及要求 (The antistatic PCB design suggestions and requests):
1. 在RoHS信号线上接TVS管, 连接TVS管(或二极管)端加瓷壳
2. 信号线走线, 如: D0, G, P0A0, 必须在一起, 同时屏蔽RoHS线
3. 注意整个PCB表面的静电防护 (Use the integrity of the entire PCB grounded).